FORM PTO-1449

LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO.	APPLICATION NO.	CONFIRMATION NO.
200209284-1	10/786,828	
APPLICANT		
David Michael Anderson		
FILING DATE	GROUP	
02/25/04	2825	

REFERENCE DESIGNATION   U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
/W	1A	6,009,251	12-28-1999	Ho et al.	
	1B	2002/0049956	04-25-2002	Bozkus et al.	
	1C	2002/0053063	05-02-2002	Bhattacharya et al.	
	1D	2002/0124232	09-05-2002	Grodd	
	1E	6,470,477 ✓	10-22-2002	Scott	
	1F	6,523,035	02-18-2003	Fleming et al.	
	1G	6,553,554	04-22-2003	Dahl et al.	
	1H	6,557,153	04-29-2003	Dahl et al.	
	1I	6,564,363 ✓	05-13-2003	Dahl et al.	
	1J	6,564,364	05-13-2003	Dahl et al.	
	1K	6,581,200	06-17-2003	Chopra et al.	

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
	1L					
	1M					
	1N					
	1O					
	1P					

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

/W	1Q	"Netlist Processing for Custom VLSI via Pattern Matching"; Thomas Stephen Chanak; Computer Systems Laboratory, Electrical Engineering Department, Stanford University; Technical Report No. CSL-TR-95-681; November 1995; 105 pages.
	1R	
	1S	

EXAMINER		DATE CONSIDERED 3/30/06